Fig. 1

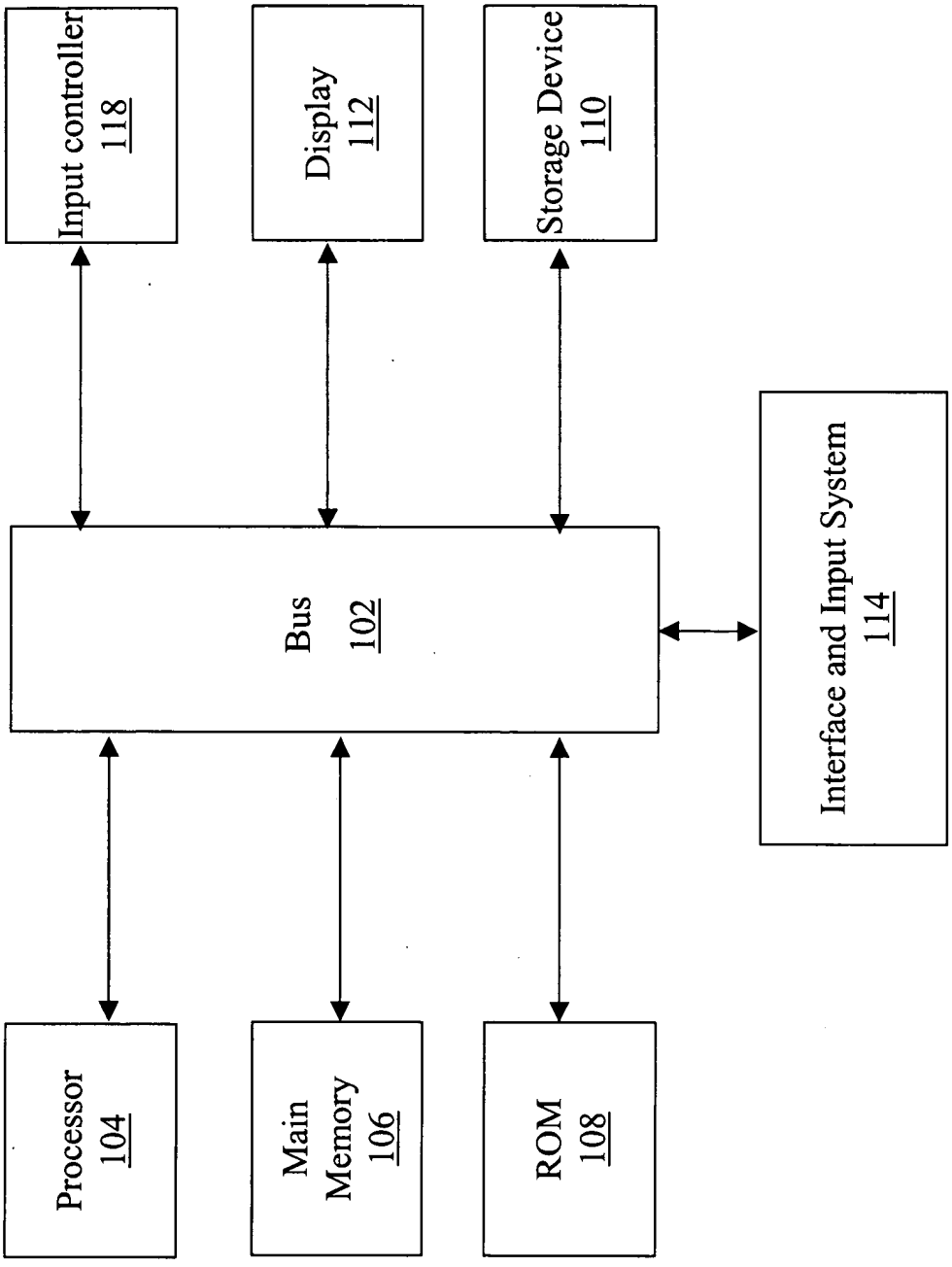
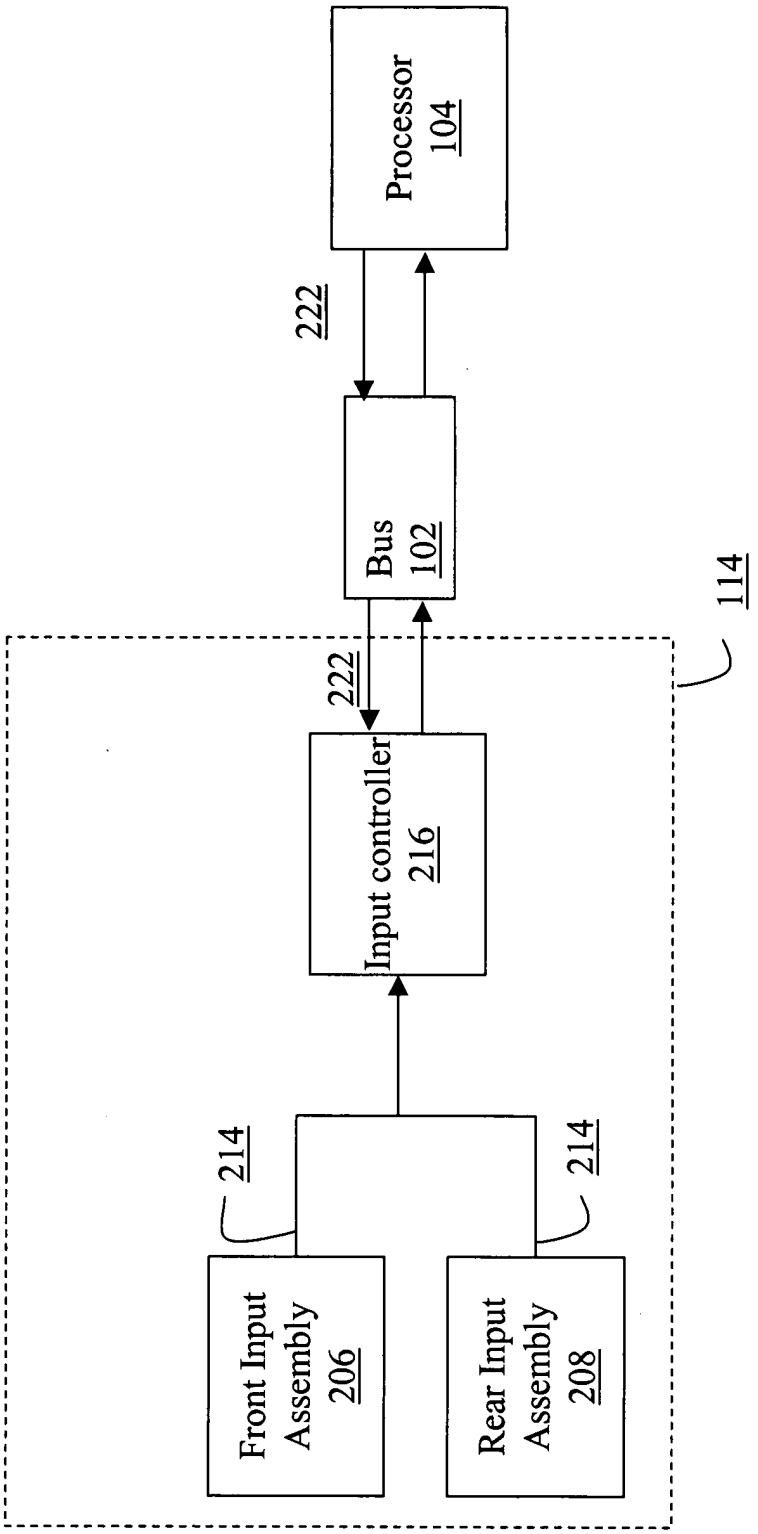

Input controller
118
Display
112
Storage Device
110
Bus
102
Interface and Input System
114
Processor
104
Main
Memory
106
ROM
108

Figure 2


Processor
104
222
Bus
102
114
222
Input controller
216
214
214
Front Input
Assembly
206
Rear Input
Assembly
208

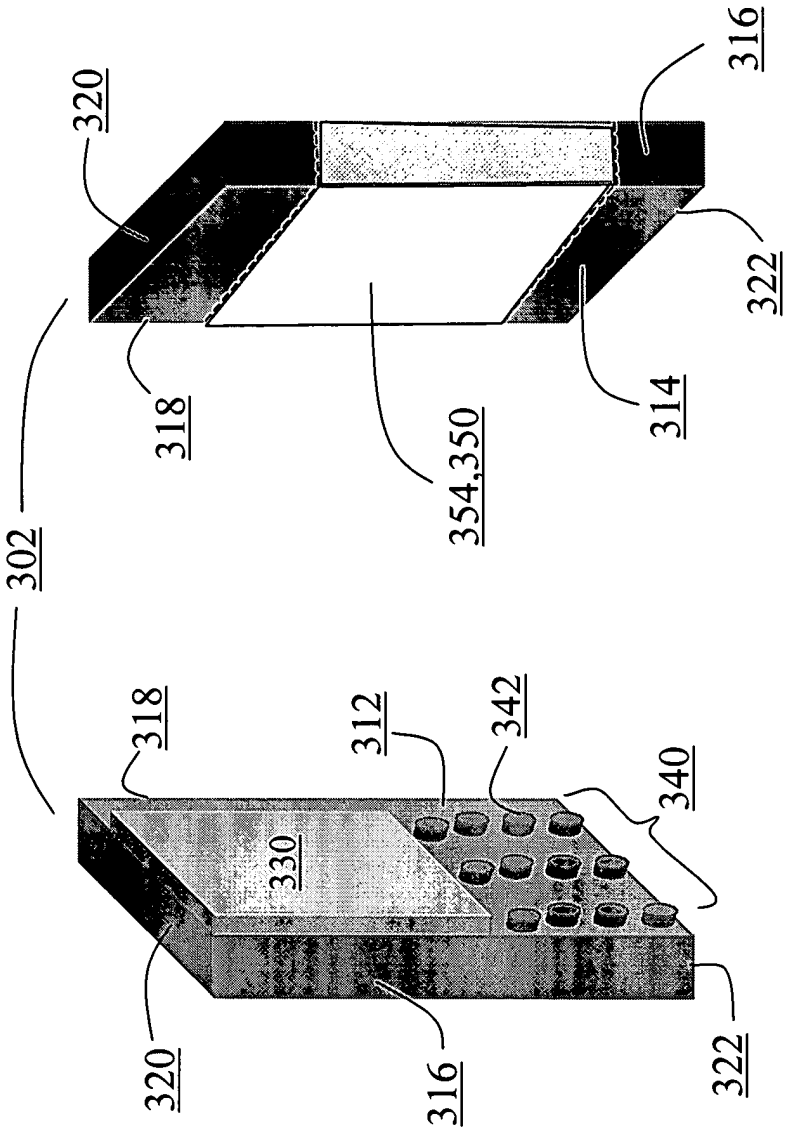



Fig 3b

Fig 3a

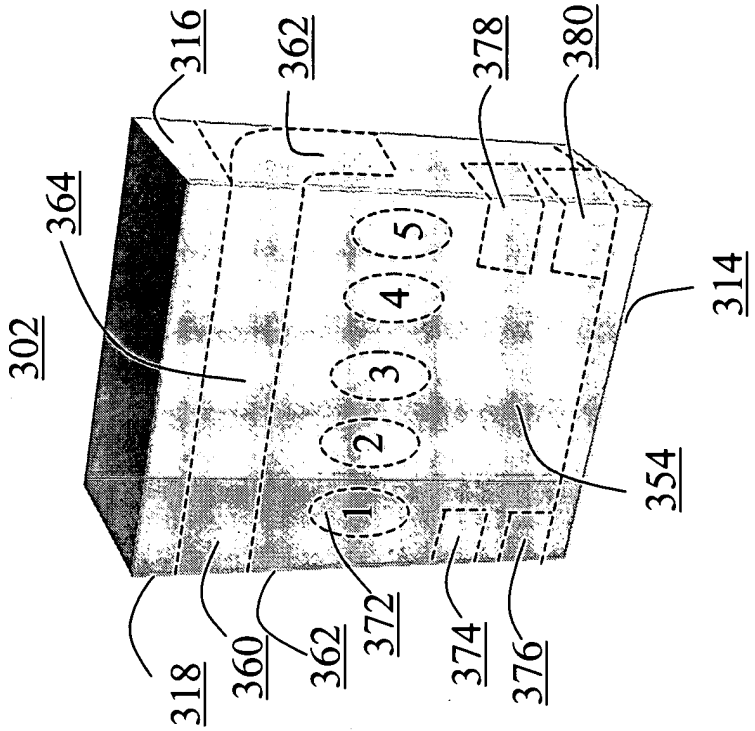

316
362
378
380
364
302
314
354
318
360
362
372
374
376
1
2
3
4
5

Fig 3d

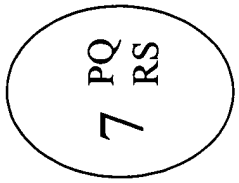

7
PQ
RS

Fig 3c

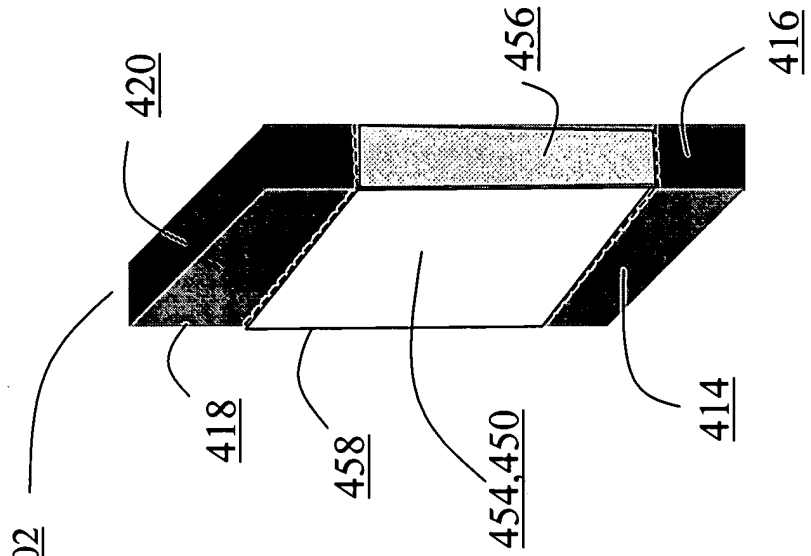



Fig 4b

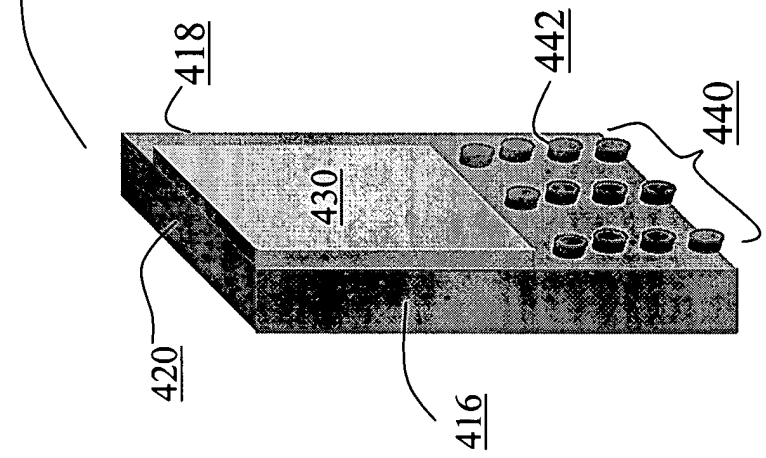



Fig 4a

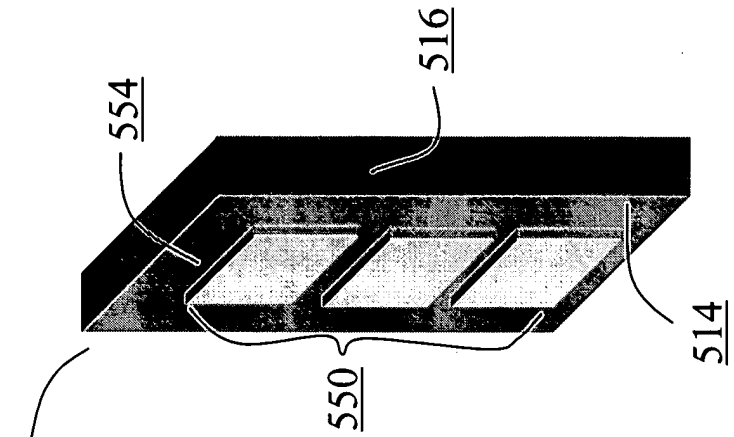



Fig. 5b

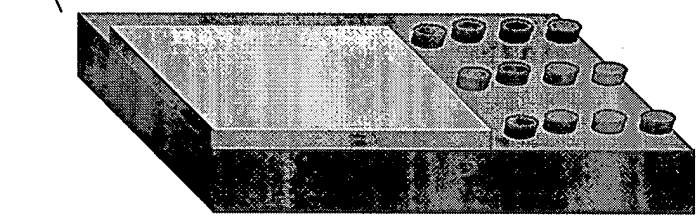



Fig 5a

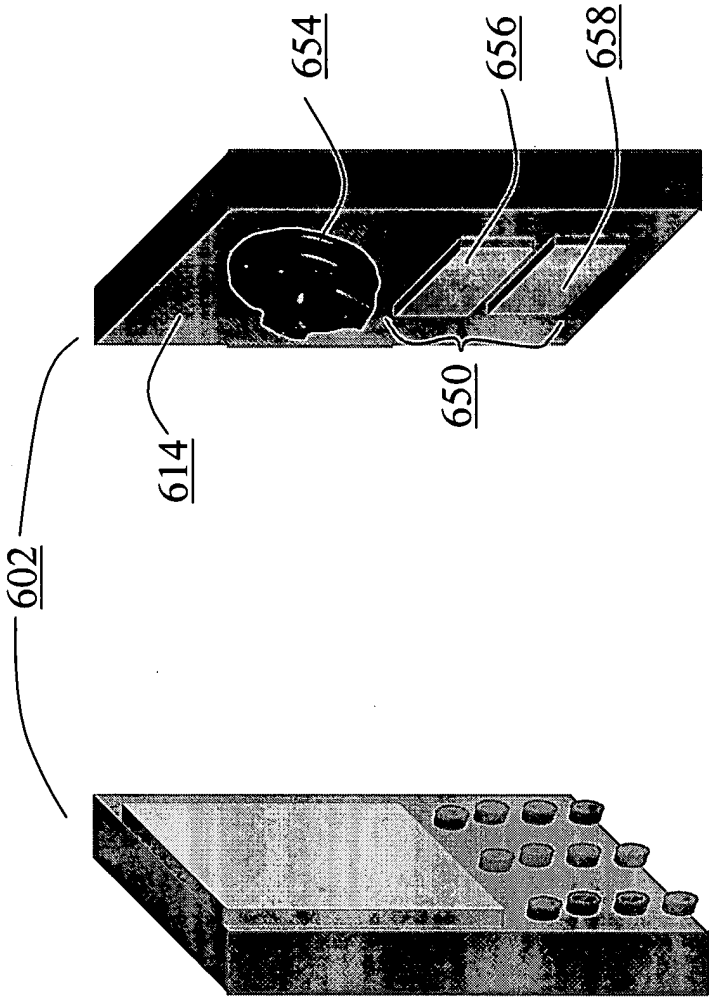



Fig. 6b

Fig 6a

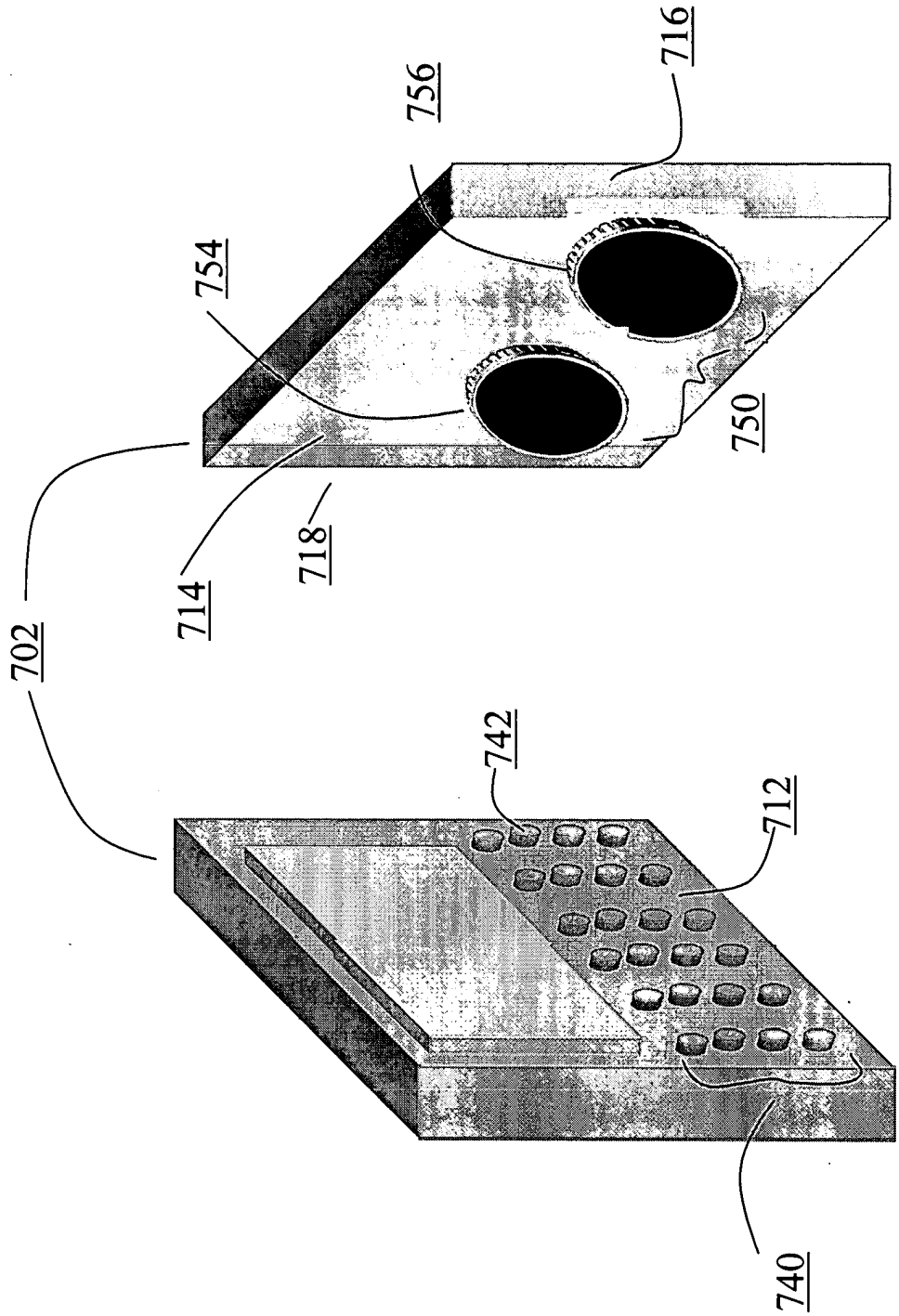
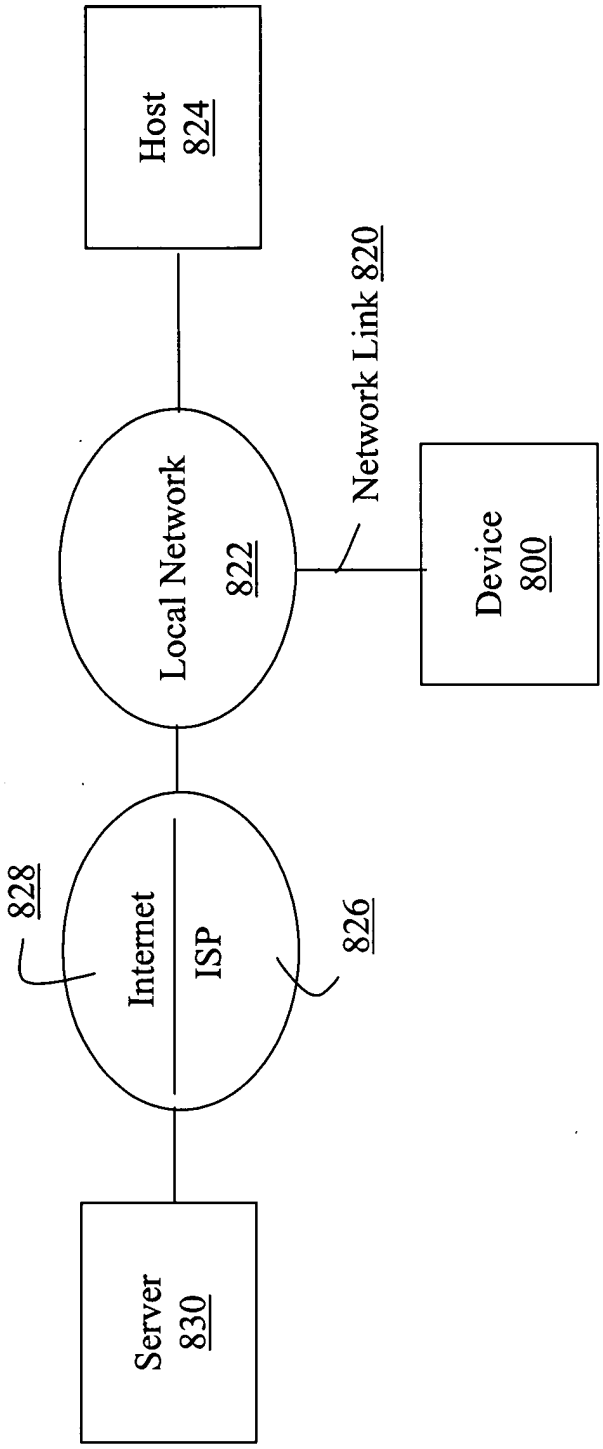



Fig. 7b

Fig 7a

Figure 8


Host
824
Network Link 820
Local Network
822
Device
800
828
Internet
ISP
826
Server
830